

Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1 IS&R	L1	0	("40372770") .PN.	USPAT	2001/09/24 10:08			0
2 IS&R	L2	1	("4037270") .PN.	USPAT	2001/09/24 10:28			0
3 IS&R	L3	1	("6181590") .PN.	USPAT	2001/09/24 10:29			0
4 BRS	L4	1	"4709317" .PN.	USPAT	2001/09/24 10:28			0
5 BRS	L5	1	"5583385" .PN.	USPAT	2001/09/24 10:29			0
6 BRS	L6	1	"5617308" .PN.	USPAT	2001/09/24 10:29			0
7 BRS	L7	1	"5933331" .PN.	USPAT	2001/09/24 10:29			0
				USPAT; US-PGPU				
8 BRS	L8	203	(board with (screw or bolt) same chip	B; EPO; JPO;	2001/09/24 10:51			0
				DERWENT ; IBM TDB				
9 BRS	L9	4	L8 and (power near chip)	USPAT; US-PGPU				
				B; EPO; JPO;	2001/09/24 11:03			0
				DERWENT ; IBM TDB				
				USPAT; US-PGPU				
10 BRS	L10	416	(board with (screws or bolt\$) same chip	B; EPO; JPO;	2001/09/24 10:43			1
				DERWENT ; IBM TDB				
				USPAT; US-PGPU				
11 BRS	L11	85	((circuit adj board) with (screw or bolt) same chip	B; EPO; JPO;	2001/09/24 10:52			0
				DERWENT ; IBM TDB				
				USPAT; US-PGPU				
12 BRS	L12	3	L11 and (power near chip)	B; EPO; JPO;	2001/09/24 11:09			0
				DERWENT ; IBM TDB				
				USPAT; US-PGPU				
13 BRS	L13	152	(power adj chip) and (circuit adj board)	B; EPO;	2001/09/24 11:10			0
				DERWENT ; IBM TDB				

Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
14 BRS	L14	49	L13 and connector	USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB	2001/09/24 11:10			0